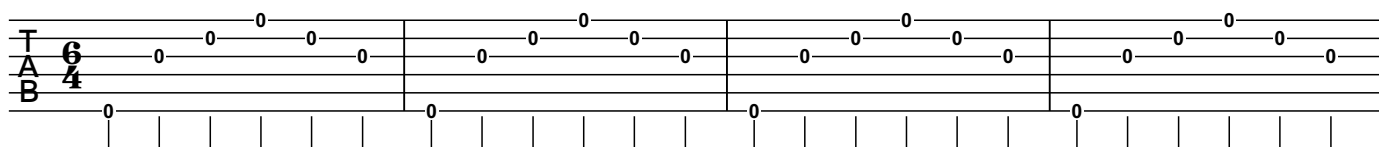


## Nothing Else Matters

Metallica

Intro

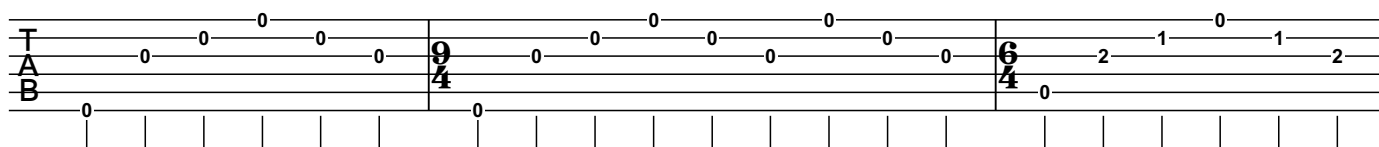
Em



Solo

Em

Am

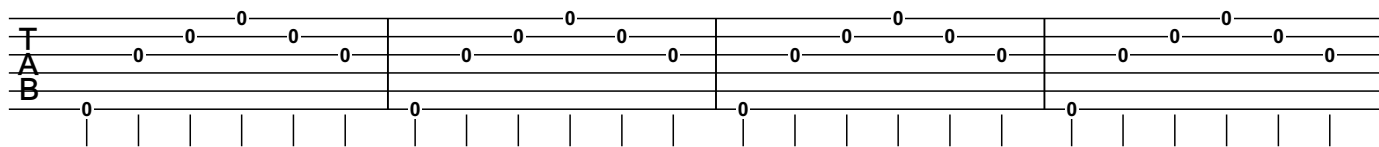


CM

DM



Em



Em

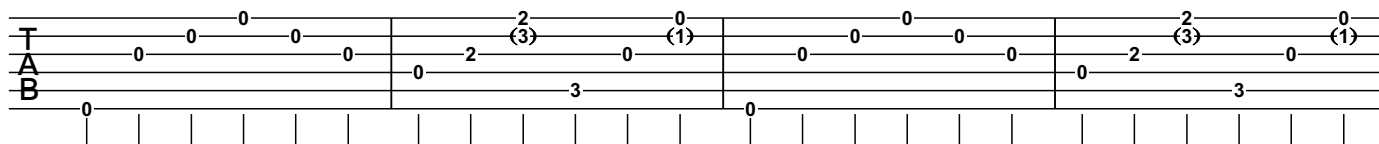
DM

CM

Em

DM

CM



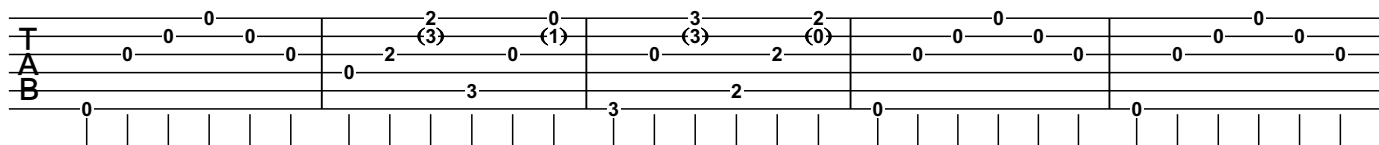
Em

DM

CM

GM B7

Em



Verses

Em

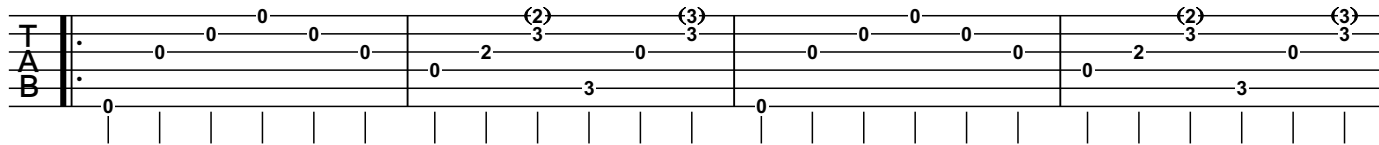
DM

Cadd9

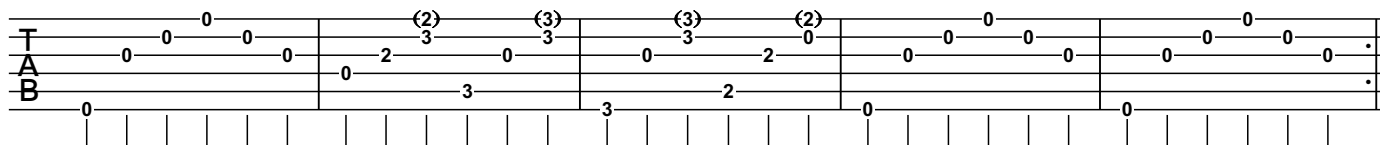
Em

DM

Cadd9

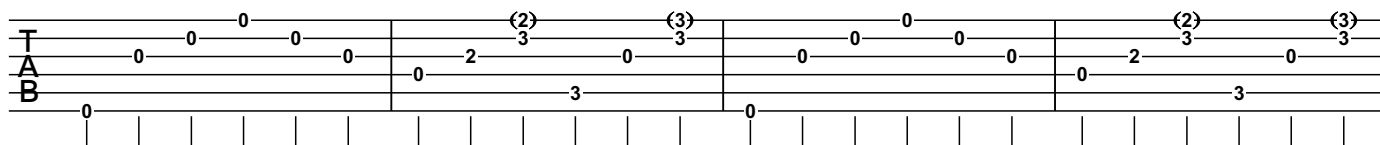


Em DM Cadd9 GM B7 Em

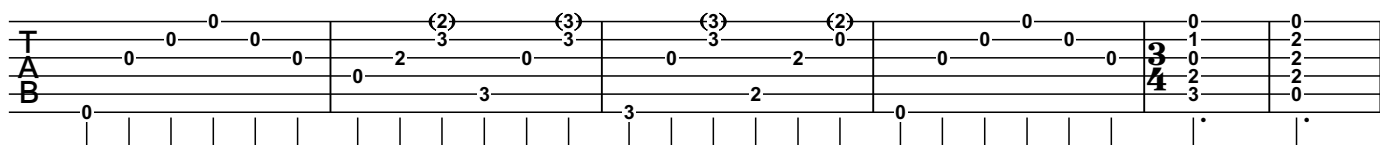


Verse

Em DM Cadd9 Em DM Cadd9

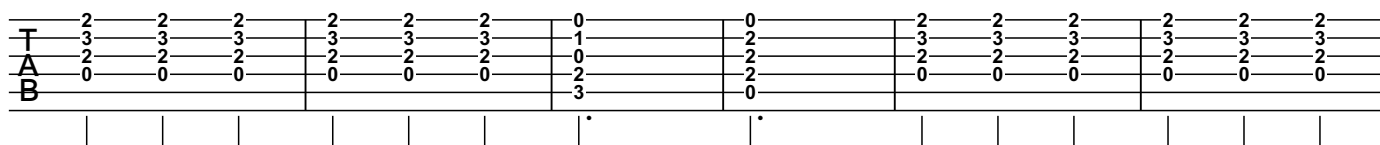


Em DM Cadd9 GM B7 Em CM AM

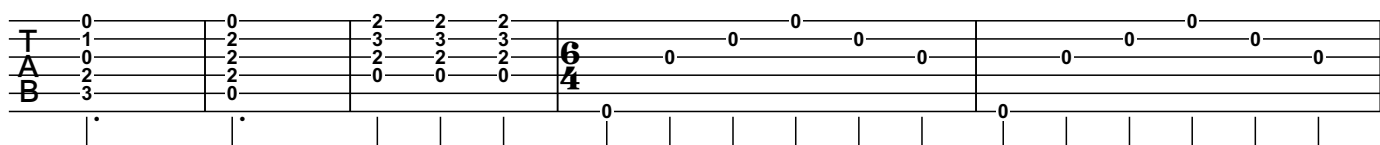


Chorus

DM CM AM DM

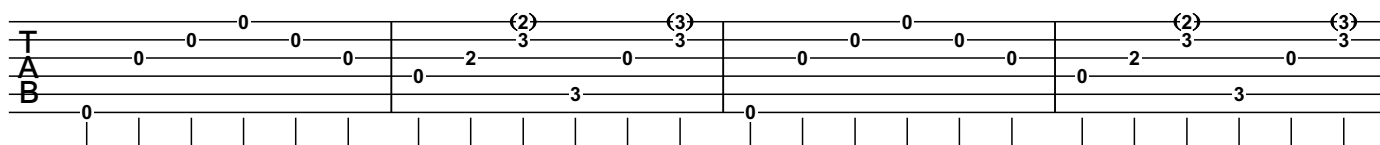


CM AM DM Em

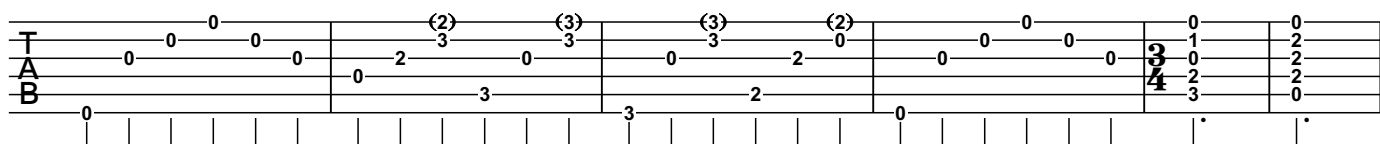


Verse

Em DM Cadd9 Em DM Cadd9

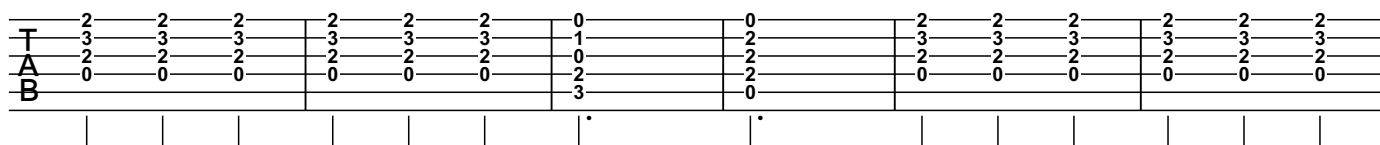


Em DM Cadd9 GM B7 Em CM AM

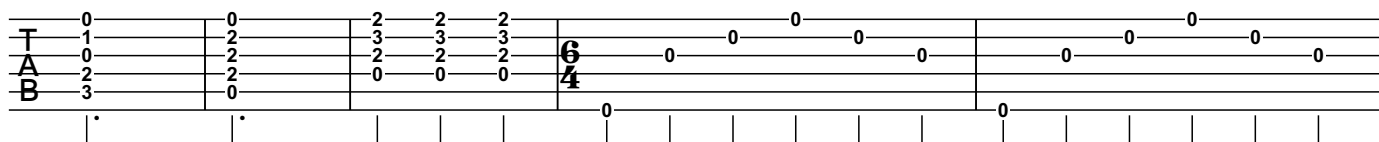


Chorus

DM CM AM DM



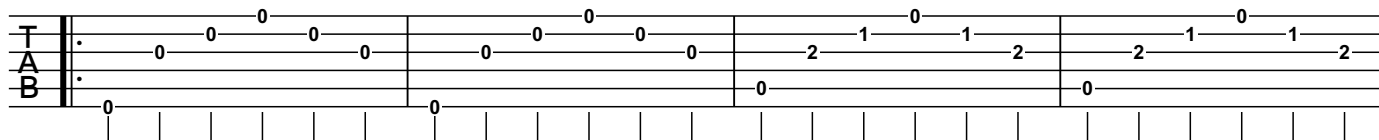
CM AM DM Em



Instrumental

Em

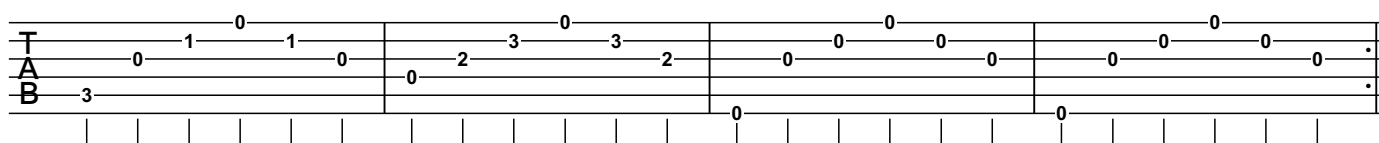
Am



CM

Dsus2

Em



Verse

Em

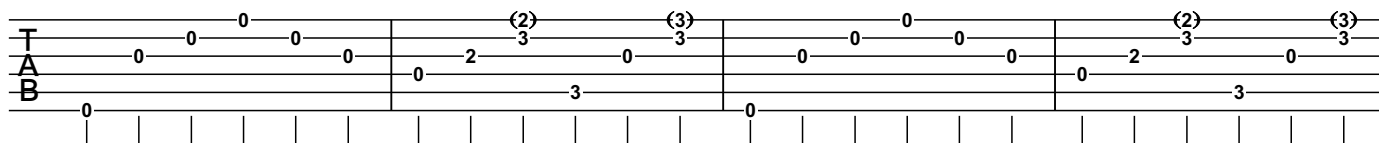
DM

Cadd9

Em

DM

Cadd9

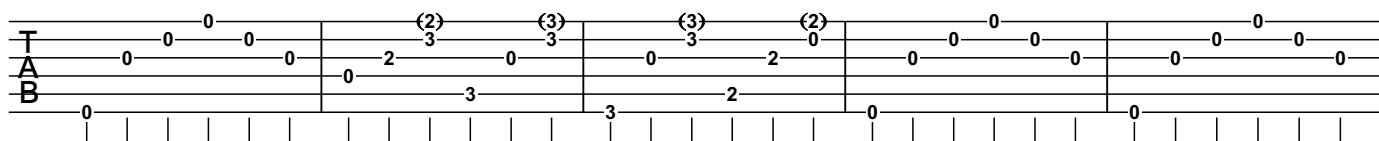


Em

DM Cadd9

GM B7

Em



Verse

Em

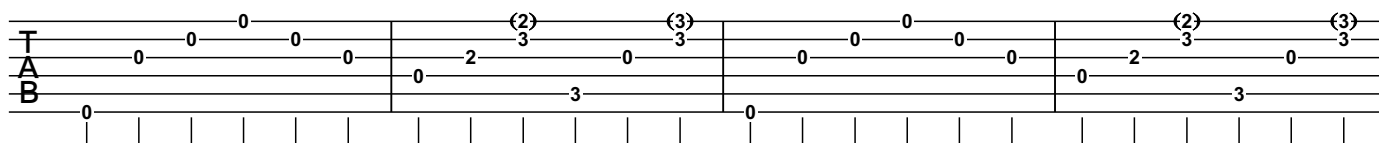
DM

Cadd9

Em

DM

Cadd9



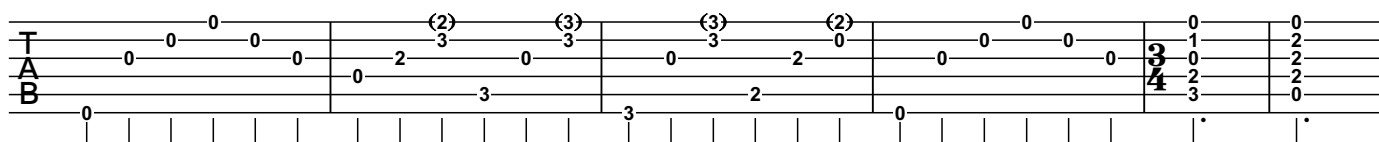
Em

DM Cadd9

GM B7

Em

CM AM



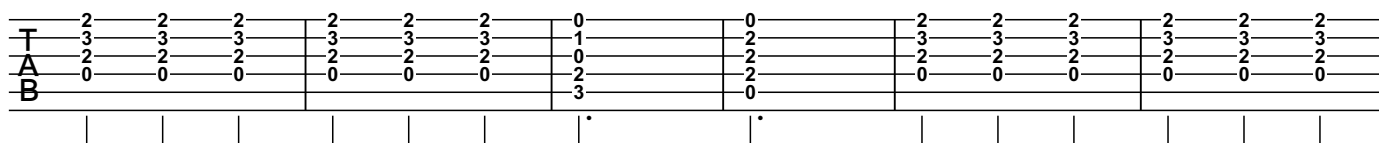
Chorus

DM

CM

AM

DM



[illegible][illegible]

Em DM Cadd9 GM B7 Em

T 0 0 0 0 0 0 0 0 0 0 0 0

A 0 0 0 0 0 2 0 0 0 2 0 0

B 0 0 0 0 0 3 0 0 0 2 0 0

0 1 2 3 4 5 6 7 8 9 10 11 12

The diagram illustrates a 32-bit bus structure. The top row, labeled 'TAB', represents a 32-bit data path divided into three 16-bit segments. The bottom row represents a single 32-bit line. Vertical lines connect the two rows at specific bit positions, indicating a mapping or selection process.

[illegible]